09/699,392

L Number	Hits	Search Text	DB	Time stamp
1	5651804	IDENTIFY$3 ADN (VALID CONNECTION$1 WITH COMPONENT$1 NEAR2 ELECTRICAL SYSTEM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:44
2	1516493	DETECT$3 AND (VALID CONNECTION WITH PROCESSOR NEAR2 ADAPTER WITH PROGRAMMABLE LOGIC CONTROLLER SYSTEM OR plc)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:47
3	1307952	(IDENTIFY$3 ADN (VALID CONNECTION$1 WITH COMPONENT$1 NEAR2 ELECTRICAL SYSTEM)) AND (DETECT$3 AND (VALID CONNECTION WITH PROCESSOR NEAR2 ADAPTER WITH PROGRAMMABLE LOGIC CONTROLLER SYSTEM OR plc))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:48
4	633036	INPUT/OUTPUT MODULES	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:49
5	2692652	EXTERNAL MACHINERY OR SENSOR$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:49
6	232009	(INPUT/OUTPUT MODULES) AND (EXTERNAL MACHINERY OR SENSOR$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:50
7	118570	((INPUT/OUTPUT MODULES) AND (EXTERNAL MACHINERY OR SENSOR$1)) AND ((IDENTIFY$3 ADN (VALID CONNECTION$1 WITH COMPONENT$1 NEAR2 ELECTRICAL SYSTEM)) AND (DETECT$3 AND (VALID CONNECTION WITH PROCESSOR NEAR2 ADAPTER WITH PROGRAMMABLE LOGIC CONTROLLER SYSTEM OR plc)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:51
8	5583981	SYSTEM BUS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:51
9	118005	(((INPUT/OUTPUT MODULES) AND (EXTERNAL MACHINERY OR SENSOR$1)) AND ((IDENTIFY$3 ADN (VALID CONNECTION$1 WITH COMPONENT$1 NEAR2 ELECTRICAL SYSTEM)) AND (DETECT$3 AND (VALID CONNECTION WITH PROCESSOR NEAR2 ADAPTER WITH PROGRAMMABLE LOGIC CONTROLLER SYSTEM OR plc)))) AND (SYSTEM BUS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:52
10	4195179	POWERED UP PROCESSOR	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:53
11	104396	((((INPUT/OUTPUT MODULES) AND (EXTERNAL MACHINERY OR SENSOR$1)) AND ((IDENTIFY$3 ADN (VALID CONNECTION$1 WITH COMPONENT$1 NEAR2 ELECTRICAL SYSTEM)) AND (DETECT$3 AND (VALID CONNECTION WITH PROCESSOR NEAR2 ADAPTER WITH PROGRAMMABLE LOGIC CONTROLLER SYSTEM OR plc)))) AND (SYSTEM BUS)) AND (POWERED UP PROCESSOR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:54
12	3588038	PRE-SPECIFIED VOLTAGE LEVEL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:54

13	4814333	PREDETERMINED VOLTAGE LEVEL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:55
14	4914698	KNOWN VOLTAGE LEVEL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:55
15	3752494	EXPECTED VOLTAGE LEVEL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:55
16	3586750	(PRE-SPECIFIED VOLTAGE LEVEL) AND (PREDETERMINED VOLTAGE LEVEL) AND (KNOWN VOLTAGE LEVEL) AND (EXPECTED VOLTAGE LEVEL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:57
17	7496711	VOLTAGE DIVIDER OR FIRST COMPONENT	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:57
18	6596240	RESISTOR OR SECOND COMPONENT	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 17:58
19	7772230	PROCESSING MODULE OR (RESISTOR OR SECOND COMPONENT)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:01
20	7534233	ADAPTER OR (VOLTAGE DIVIDER OR FIRST COMPONENT)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:11
21	6596240	(RESISTOR OR SECOND COMPONENT) AND (PROCESSING MODULE OR (RESISTOR OR SECOND COMPONENT))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:14
22	6233187	(PROCESSING MODULE OR (RESISTOR OR SECOND COMPONENT)) AND (ADAPTER OR (VOLTAGE DIVIDER OR FIRST COMPONENT))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:17
23	2103008	((RESISTOR OR SECOND COMPONENT) AND (PROCESSING MODULE OR (RESISTOR OR SECOND COMPONENT))) AND ((PRE-SPECIFIED VOLTAGE LEVEL) AND (PREDETERMINED VOLTAGE LEVEL) AND (KNOWN VOLTAGE LEVEL) AND (EXPECTED VOLTAGE LEVEL))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:26
24	6381	714/721.CCLS. OR 714/724.CCLS. OR 714/733.CCLS. OR 714/733.CCLS. OR 714/736.CCLS. OR 714/799.CCLS. OR 324/?.CCLS. OR 365/?.CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/12 18:32

| 25 | 2331 | (((RESISTOR OR SECOND COMPONENT) AND (PROCESSING MODULE OR (RESISTOR OR SECOND COMPONENT))) AND ((PRE-SPECIFIED VOLTAGE LEVEL) AND (PREDETERMINED VOLTAGE LEVEL) AND (KNOWN VOLTAGE LEVEL) AND (EXPECTED VOLTAGE LEVEL))) AND (714/721.CCLS. OR 714/724.CCLS. OR 714/733.CCLS. OR 714/733.CCLS. OR 714/736.CCLS. OR 714/799.CCLS. OR 324/?.CCLS. OR 365/?.CCLS.) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/02/12 18:33 |